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9333 S. JOHN YOUNG PARKWAY, ORLANDO FLORIDA 32819-8698

PHONE: 407-371-3336 FAX: 407-371-3781

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Serial No. 09/648,164

**IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE**

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Patent Application

**Inventor(s): Chyan, Yih Feng
Hergenrother, John
Monroe, Donald**

Case: 15-6-9

Serial No.: 09/648164

Filing Date: August 25, 2000

Examiner: Dickey

Group Art Unit: 2826

Title: Architecture for Circuit Connection Of A Vertical Transistor

I hereby certify that this Amendment is being
faxed to the United States Patent Office on
November 5, 2004 to fax number 703-872-9318
addressed to this Commissioner of Patents and
Trademarks Washington, D.C. 20231

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**ASSISTANT COMMISSIONER FOR PATENTS
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SIR:

Preliminary Amendment

In response to the office action dated May 5, 2004, please amend the above application as follows: